

Attorney Docket No. TT-34791

REMARKS

Claims 1-21 remain in prosecution and claims 20 and 21 have been allowed. A one month extension of time and appropriate fee are enclosed with this amendment.

Claims 2-9, 12-19 were objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. Claims 2-9 and 12-19 have been rewritten in independent form including all of the limitations of the base claims and any intervening claims, as such, claims 2-9 and 12-19 are believed to be in condition for allowance.

Claims 1, 10 and 11 were rejected under 35 U.S.C. 102(b) as being anticipated by *Veale* (U.S. Pat. No. 4,458,322). Claim 10 has been amended to depend on currently amended claim 2 and as such it is believed to be in condition for allowance.

Independent claim 1 has been amended to recite that the DC offset estimate determined by a DC estimation circuit is stored then the input signal is compensated using the DC offset estimate and the compensated signal is scaled and the scaled compensated input signal is provided back again to the DC estimation circuit in order to perform a fine DC offset estimation on the scaled compensated input signal. Support for the amendment can be found at FIG. 2 and its accompanying discussion starting at paragraph 30 (page 10) to paragraph 35 (page 12). None of the cited references teach or suggest a method of DC offset estimation where a DC offset circuit is reused for performing multistage DC offset estimations as claimed. As such claim 1 is believed to be in condition for allowance.

Independent claim 10 has been amended to recite "a selector circuit for receiving the input signal and scaled signal and providing the scaled signal to the DC estimation block in order for the DC estimation block to perform a fine DC offset estimation of the scaled signal". In the embodiment shown in Fig. 2, the multiplexer 48 or 50 acts as a selector circuit for providing the input signal I_{IF} or Q_{IF} or the scaled compensated signal $(I_{IF} - S)$ or $(Q_{IF} - S)$ to the DC estimation block 54. This allows the DC estimation block 54 to be reused in order to perform stage 1 and stage 3 DC offset estimations. Since it is believed that none of the cited references teach or suggest an apparatus for DC offset estimation as claimed in claim 10, it is believed that claim 10

is in condition for allowance.

Applicants respectfully submit that the present application is in condition for allowance. If the Examiner has any questions or comments or otherwise feels it would be helpful in expediting the application, he is encouraged to telephone the undersigned at (972) 731-2288.

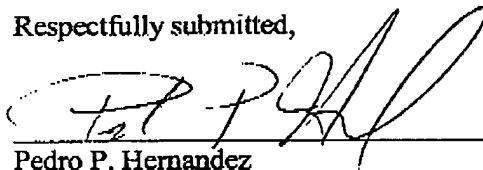
The Commissioner is hereby authorized to charge payment of any further fees associated with any of the foregoing papers submitted herewith, or to credit any overpayment thereof, to Deposit Account No. 20-0668, Texas Instruments, Inc.

Date:

10/11/04

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Respectfully submitted,



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